

Curriculum Vitae

Goran Jovanović

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A. PERSONAL DATA

Birth date and Place: September 7, 1963, Leskovac, Serbia

Work Address: Faculty of Electronic Engineering, Aleksandra Medvedeva 14, P.O. Box 73, 18000 Niš, Serbia

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B. EDUCATIONAL HISTORY

June 1989 University of Nis, Faculty of Electronic Engineering
Nis, Serbia
Dipl. Ing. in Electronic Engineering (5 years degree)

June 1995 University of Nis, Faculty of Electronic Engineering
Nis, Serbia
M.S.E. in Electronic Engineering,
Thesis: "Realization of Arctangent detector based on Digital Signal Processing for Signal Processor TMS320C25"

June 2006 University of Nis, Faculty of Electronic Engineering
Nis, Serbia
PhD in Electronic Engineering,
Thesis: "Linear Voltage Controlled Delay Lines for DLL Applications"

C. PROFESSIONAL POSITIONS

1990 - 1996 University of Nis, Faculty of Electronic Engineering, Nis, Serbia
Position: **Junior Teaching Assistant.** *Department of Electronics.*
Duties: Lectured and counseled students in three separate courses: "Radio Engineering", "Radio Transmitter and Receiver" and "Electronics II".

1996 – 2006 University of Nis, Faculty of Electronic Engineering, Nis, Serbia
Position: **Research and Teaching Assistant.** *Department of Electronics.*
Duties: Lectured and counseled students in five separate courses: "Radio Engineering", "Radio Transmitter and Receiver", "High-Frequency

Electronics”, “Radio–Communication Systems” and “Electronics II”.
Composed exams and term paper assignments, grade all written work.

Research: Wireless Electronic, Analog Electronic, DSP, Hardware and Software Design.

2007-2009

University of Nis, Faculty of Electronic Engineering, Nis, Serbia

Position: Assistant Professor. Department of Electronics.

Duties: Lectured and counseled students in six separate courses: “RF electronic”, “Design of RF Architecture”, “Radio Communication Systems”, “Mobile Communication”, “Television and Radio” and “Multimedia Production”. Composed lecture, exams and term paper assignments, grade all written work.

Research: RF/MW Electronic, Analog Electronic, DSP, Hardware and Software Design.

D. PARTICIPATION IN RESEARCH PROJECTS

1991 – 1994 *Project name:* Satellite and Cable Television and High Resolution Television

Financial support: Serbian Ministry of Science and Technology

1994 – 1996 *Project name:* Satellite and Cable Television and High Resolution Television

Financial support: Serbian Ministry of Science and Technology

1995 – 1996 *Project name:* Frame Clock with Remote Programming

Financial support: Serbian Ministry of Science and Technology

1995 – 1996 *Project name:* Audio Amplifier Design

Financial support: Serbian Ministry of Science and Technology

1998 – 2001 *Project name:* Processing and Transfer of Digital Signal in Access Plane of Telecommunication Network

Financial support: Serbian Ministry of Science and Technology

2002 – 2004 *Project name:* Ultrasonic Flowmeter for liquid measurement

Financial support: Serbian Ministry of Science and Technology

2005 – 2007 *Project name:* Wireless Sensor Network

Financial support: Serbian Ministry of Science and Technology

2007 – 2009 *Project name:* Reconfigurable Computer Systems

Financial support: Serbian Ministry of Science and Technology

E. REFERENCES

Journal Papers

- [1] Mile Stojčev and **Goran Jovanović**, “Clock aligner based on delay locked loop with double edge synchronization”, *Microelectronics Reliability*, Vol. 48, No. 1, pp. 158–166, January 2008.
- [2] **G. Jovanović**, D. Mitić, M. Stojčev, *An adaptive pulse-width control loop*, *International Journal of Electronics*, pp. 291 - 311, Vol. 93, No. 5, May 2006.
- [3] **G. S. Jovanović** and M. K. Stojčev, *Current starved delay element with symmetric load*, *International Journal of Electronics*, pp. 167-175, Vol. 93, No 3, March 2006.
- [4] **Goran Jovanović**, Mile Stojčev, *Pulsewidth Control Loop as a Duty Cycle Corrector*, *Serbian Journal of Electrical Engineering*, Vol. 1, No. 2, pp. 215-226, June 2004.
- [5] **Goran S. Jovanović**, Mile K. Stojčev, *Voltage Controlled Delay Line for Digital Signal*, *Facta Universitatis, Series: Electronics and Energetic*, vol 16. No. 2, pp. 215-232, August 2003.
- [6] Marina M. Paunović, Dragiša V. Krstić, **Goran S. Jovanović**, *Choke Inductor Value Influence on the Frequency Response and Amplitude Modulation Characteristics of the Class E Power Amplifier*, *Facta Universitatis Niš, Series: Electronics and Energetic*, vol 12. No 1, pp. 41-53, 1999.
- [7] E. I. Milovanović, I. Ž. Milovanović, M. K. Stojčev, **G. S. Jovanović**, *Fault-Tolerant Matrix Inversion on Processor Array*, *Electronics Letters*, vol. 28, No. 13, pp. 1206-1208, 1992.

Conference Papers

- [1] **Goran Jovanović** and Mile Stojčev, “A Method for Improvement Stability of a CMOS Voltage Controlled Ring Oscillators”, *ICEST 2007, Proceedings of Papers*, vol. 2, pp. 715-718, Ohrid, Jun 2007.
- [2] **Goran S. Jovanović** and Mile K. Stojčev, *Delay Locked Loop with Double Edge Synchronization*, *ICEST 2006*, pp. 286-289, Sofia, Jun 2006.
- [3] **G. Jovanović**, D. Mitić, and M. Stojčev, *An Adaptive Pulse-Width Control Loop*, *Proc. 25th International Conference on Microelectronics (MIEL 2006)*, vol. 2, pp. 667-670, Belgrade, Serbia and Montenegro, 14-17 May, 2006.
- [4] **Goran Jovanović**, Mile Stojčev, Dragiš Krstić, *Delay Locked Loop with Linear Delay Element*, in *Proc. of 7-th International Conference TELSIKS*, vol. 2, pp. 397-400, Niš, Serbia and Montenegro, September 2005.
- [5] **Goran Jovanović**, Mile Stojčev, *High-Speed Pulse-Width Control Loop*, *INDEL 2004*, vol. 1, pp. 89-94, Banja Luka, November 2004.