AGENDA

DAAD Workshop

"Embedded System Design"

Nis, Serbia

29 June - 3 July 2009

Program

Time	Content	Speaker/Contributor
Start of the workshop 29th June in Faculty of Electronic Eng		
Monday - 29th June - 1st day		
9:15-9:30	Welcome, Workshop Program, Introduction to ELFAK	Prof. M. Stojcev, ELFAK
9:30-10:30	Introduction to Wireless Communication Systems	Prof. R. Kraemer, IHP
10:30-10:45	Coffee Break	All
10:45-11:45	Introduction to Wireless Communication Systems	Prof. R. Kraemer, IHP
11:45-13:00	Lunch break	
13:00-14:00	WLAN & High Performance Communication Systems	Prof. R. Kraemer, IHP
Tuesday - 30th June - 2nd day		
9:30-10:30	WLAN & High Performance Communication Systems	Prof. R. Kraemer, IHP
10:30-10:45	Coffee Break	All
10:45-11:45	Wireless Sensor Networks	Prof. R. Kraemer, IHP

11:45-13:00	Lunch break	
13:00-14:00	Wireless Sensor Networks	Prof. R. Kraemer, IHP
Wednesday - 1st July - 3rd day		
9:30-10:30	SoC Design - Modelling, Simulation & Verification	Dr. Zoran Stamenkovic, IHP
10:30-10:45	Coffee Break	All
10:45-11:45	SoC Design - Integrated Circuits & RAMs	Dr. Zoran Stamenkovic, IHP
11:45-13:00	Lunch break	
13:00-14:00	Asynchronous Circuit Design	Dr. Milos Krstic, IHP
Thursday - 2nd July - 4th day		
9:30-10:30	Asynchronous Circuit Design	Dr. Milos Krstic, IHP
10:30-10:45	Coffee Break	All
10:45-11:45	Globally Asynchronous Locally Synchronous Systems	Dr. Milos Krstic, IHP
11:45-13:00	Lunch break	
13:00-14:30	Synchronous & GALS Networks on Chip	Dr. Milos Krstic, IHP
Friday - 3rd July - 5th day		
9:30-10:30	SoC Design - Synthesis & Layout	Dr. Zoran Stamenkovic, IHP
10:30-10:45	Coffee Break	All
10:45-11:45	SoC Design - DfT & DfM	Dr. Zoran Stamenkovic, IHP
11:45-13:00	Lunch break	
13:00-13:30	SoC Design - Configurable Processors	Dr. Zoran Stamenkovic, IHP
13:30-14:00	End of the Workshop, closing remarks and discussion	Prof. Mile Stojcev

Rolf Kraemer received the diploma and the Dr.-Ing. degrees from the computer science department of the RWTH-Aachen in Germany. He has worked for 15 years in research and development of communication and multi-media systems at Philips-Research in Hamburg and Aachen. Since 1998 he is Professor of Systems at the IHP in Frankfurt and TU-Cottbus. He leads the systems research department of the IHP where his research focus is on wireless Internet systems from application to systems on chip. He is co-founder of the startup-company lesswire AG where he holds the position of the CTO.

Miloš Krstić received the Dr-Ing. degree in electronics from Brandenburg University of Technology, Cottbus, Germany in 2006. Since 2001 he has been with IHP Microelectronics, Frankfurt (Oder), Germany. For the last few years, his work was mainly focused on low power digital design for wireless applications and GALS methodologies for digital systems integration. His work was followed with more than 40 publications and 7 patent applications mainly in the area of GALS design and its application to the wireless communication area.

Dr. Zoran Stamenković is with IHP GmbH, Frankfurt (Oder), Germany. He received his Ph.D. degree in electronic engineering from the University of Niš, Serbia in 1995. His research interests include wireless SOC design, HDL modeling, logic synthesis, chip layout, and IC yield modeling and prediction. Currently, he is co-leading the EU funded project on a wireless MIMO system (MIMAX) at IHP GmbH. He has published a book on IC yield, two chapters on IC yield and testing in the Computer Engineering Handbook (Outstanding Academic Title for 2002 by Choice Magazine, USA), and more than 40 scientific journal and conference papers.