AGENDA

DAAD Meeting

"Embedded System Design"

Nis, Serbia

10 Mai 2011

Program

Time	Content	Speaker/Contributor
9:00-9:15	Welcome, Agenda introduction, Introduction to ELFAK	Prof. M. Stojcev, ELFAK
9:15-10:00	DAAD Project "Embedded System Design" - current state, plan for 2011 and beyond	Dr. Milos Krstic, IHP
10:00-10:30	Overview of the existing cooperation and possibilities for the future, perspectives/plans for the joint PhD/Master supervision and collaborative projects	Prof. R. Kraemer, IHP
10:30-11:00	Round tour through ELFAK labs	Prof. Mile Stojcev, all
11:00-11:15	Coffee Break	All
11:15-11:30	FEEIT outlook to the existing cooperation and possibilities for the future in respect to the common PhD/Master supervision and collaborative projects	Prof. J.Kjosev, FEEIT
11:30-11:45	ELFAK outlook to the existing cooperation and possibilities for the future in respect to the common PhD/Master supervision and collaborative projects	Prof. M. Stojcev, ELFAK
11:45-12:00	ETF outlook to the existing cooperation and possibilities for the future in respect to the common PhD/Master supervision and collaborative projects	Doc. Slobodan Lubura, ETF
12:00-12:30	Discussion, future plans, wrap-up	All
12:30-12:45	IP Protection in DAAD Project	Dr. M. Krstic, IHP
12:45-13:00	Coffee Break	all
13:00-14:00	Lecture Prof. Rolf Kraemer "OMEGA I-MAC: A Protocol Architecture for Heterogeneous high speed Home Networks"	open for visitors outside of the project team
14:00-22:00	Lunch, Sightseeing, and possibly Dinner	all

OMEGA I-MAC: A Protocol Architecture for Heterogeneous high speed Home Networks

The talk will outline the current state of home networks and the problems involved with different communication technologies. Especially the use of wireless communication only results in problems both with respect to QoS and coverage. So the I-Mac approach was chosen within the FP7 project OMEGA. I-Mac unifies the network topology view and allows the simple configuration of heterogeneous networks with seamless handover between the different technologies. It has been implemented and tested within a complex demonstrator up to a speed of almost 1Gb/s. Within this talk the architecture and the different engines are shortly described. The demonstrator scenario will be shown as well as the measurements that describe expected behaviour in terms of additional latency for multihop.

Rolf Kraemer received the diploma and the Dr.-Ing. degrees from the computer science department of the RWTH-Aachen in Germany. He has worked for 15 years in research and development of communication and multi-media systems at Philips-Research in Hamburg and Aachen. Since 1998 he is Professor of Systems at the IHP in Frankfurt and TU-Cottbus. He leads the systems research department of the IHP where his research focus is on wireless Internet systems from application to systems on chip. He is co-founder of the startup-company lesswire AG.